Please amend claims 96 and 201 as follows.

96.   (amended) A computer implemented learning method using a digital computer which includes a processor and a memory, comprising the steps of:

(a) selecting a directly executable instruction <u>which includes run time data</u>;

(b) determining a binary number which represents the directly executable instruction to the processor;

(c) selecting a location in the memory;

(d) creating a directly executable machine code entity which includes the binary number;

(e) storing the entity as data in said location;

(f) providing an indirectly executable instruction which causes the processor to directly execute machine code stored in said location;

(g) controlling the processor to process the indirectly executable instruction, resulting in the processor directly executing the entity as including the directly executable instruction;

(h) altering the entity as data such that the binary number is changed to represent a different directly executable machine code program instruction to the processor; and

(i) repeating steps (c) to (h) until an end criterion is reached.

201. (amended) A Turing complete computer implemented learning method comprising the steps of:

(a) providing a Turing complete computer with an indirectly executable program including:

a first instruction that points to and designates machine code stored in a memory as data;

a second instruction that points to and designates machine code stored in a memory as directly executable code;

a third instruction that alters machine code pointed

to by the first instruction; and

a fourth instruction that executes machine code pointed to by the second instruction; and

(b) controlling the computer to execute the program which performs the steps of:

(b1) creating and storing a machine code entity including a directly executable instruction <u>which includes run time data</u> in a memory;

(b2) executing the second instruction to point to the entity;

(b3) executing the fourth instruction using input data to produce a result;

(b4) evaluating the result;

(b5) executing the first instruction to point to the entity;

(b6) executing the third instruction to alter the entity to include a different directly executable instruction; and

(b7) repeating steps (b3) to (b6) until an end criterion is reached.

REMARKS

The Examiner is thanked for the clarity and conciseness of the official action and for the citation of the reference to P. Nordin which has been duly considered. The claims have been amended to point out even more clearly what are believed to be Applicants' most important contributions to the art. Reconsideration of the invention is respectfully requested.

Applicants are pleased to note the allowance of claims 1-41, 43-46, 48-72, 74-77, 79-95, 167-182, 184-200 and 202, and the Examiner's indication that claims 97-105, 107-112, 114, 116-119, 121 and 123-166 are objected to as being dependent on a rejected base claim.